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JC872 U.S. PTO  
10/051280  
01/22/02

**Box PATENT APPLICATION**  
**Assistant Commissioner for Patents**  
**Washington, DC 20231**

New U.S. Patent Application  
Title: MODULAR ARITHMETIC APPARATUS AND METHOD SELECTING A  
BASE IN A RESIDUE NUMBER SYSTEM  
Inventor and Address:

1) **Atsushi SHIMBO**  
Tokyo, Japan

Sir:

We enclose the following papers for filing in the United States Patent and Trademark Office in connection with the above patent application.

1. Application- 24 pages, including 7 independent claims and 20 claims total.
2. Drawings- 4 sheets of drawings containing 6 figures.
3. The filing fee is calculated as follows:

Basic Application Filing Fee					\$740	\$ 740.00
	Number of Claims		Basic	Extra Claims		
Total Claims	20	-	20	0	x \$18	\$
Independent Claims	7	-	3	4	x \$84	\$ 336.00
<input type="checkbox"/> Presentation of Multiple Dep. Claim(s)					+\$280	
Subtotal						\$ 1076.00
Reduction by 1/2 if small entity						-
TOTAL APPLICATION FILING FEE						\$ 1076.00

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January 22, 2002

Page 2 of 2

4. A check for \$1076.00 is enclosed. The fee includes:

\$740.00 filing fee; and

\$336.00 additional claims fee.

This application is being filed under the provisions of 37 C.F.R. § 1.53(f). Applicant awaits notification from the Patent and Trademark Office of the time set for filing the Declaration.

Applicant claims the right to priority based on Japanese Patent Application No. 2001-013564, filed on January 22, 2001.

Please address all correspondence with respect to this application to:

Finnegan, Henderson, Farabow,  
Garrett & Dunner, L.L.P.  
1300 I Street, N.W.  
Washington, D.C. 20005-3315

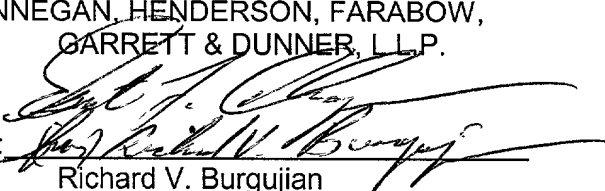
Please accord this application an application number and filing date.

The Commissioner is hereby authorized to charge any additional filing fees due and any other fees due under 37 C.F.R. § 1.16 or §1.17 during the pendency of this application to our Deposit Account No. 06-0916.

Respectfully submitted,

FINNEGAN, HENDERSON, FARABOW,  
GARRETT & DUNNER, L.L.P.

By:

  
Richard V. Burgujian  
Reg. No. 31,744

RVB/FPD/sci  
Enclosures

ERNEST F. CHAPMAN  
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